RECEIVED
CENTRAL FAX CENTER
DEC 2 6 2007

IN THE SPECIFICATION

Please amend the following paragraphs in the published application as follows.

[0018] Intel has published a standard for MultiProcessor systems known as "MultiProcessor Specification" version ~~1.4~~ 1.4, 1997, which is incorporated herein by reference in its entirety ~~and may be accessed at http://developer.intel.com/design/pro/datashts/2420 1606.pdf~~. This specification describes an architecture where memory and peripherals are symmetrically shared between multiple processors. Machines are readily available, which conform to this standard, in the form of multi-processor Personal Computers (PCs). The intention of the MultiProcessor Standard is for a single copy of the operating system and applications to run on all/any processors in the system. This architecture is known as Symmetric MultiProcessing.

[0022] This invention in one embodiment incorporates use of NTP; Network Time Protocol (STD0012, rfc1305 by David L. Mills, University of Delaware, March 1992). ~~It also enhances the claims made in a related patent disclosure, "Video Streaming Server Utilizing Asymmetric architecture on a Symmetric Multiprocessing platform"~~. NTP provides the mechanisms to synchronize time and coordinate time distribution in a large, diverse internet operating at rates from mundane to lightwave. It uses a returnable-time design in which a distributed subnet of time servers operating in a self-organizing, hierarchical-master-slave configuration synchronizes local clocks within the subnet and to national time standards via wire or radio. The servers can also redistribute reference time via local routing algorithms and time daemons.

617795-1